

# HITJ0204MP

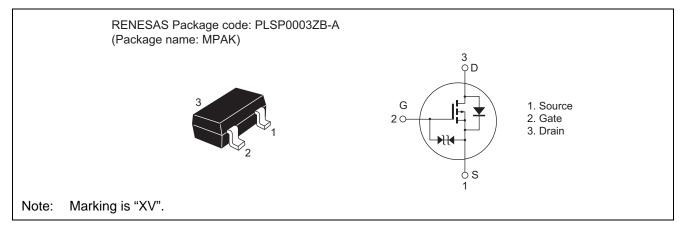
-20V, -1.6A, 280mΩmax. Silicon P Channel MOS FET Power Switching

R07DS0476EJ0200 Rev.2.00 May 09, 2013

#### Features

- Low on-resistance
- $R_{DS(on)} = 219 \text{ m}\Omega \text{ typ} (V_{GS} = -4.5 \text{ V}, I_D = -0.8 \text{ A})$
- Low drive current
- High speed switching
- 2.5 V gate drive

### Outline



## **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
ltem	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	-20	V
Gate to source voltage	V <sub>GSS</sub>	+8 / -12	V
Drain current	I <sub>D</sub>	-1.6	А
Drain peak current	I <sub>D(pulse)</sub> Note1	-4.0	А
Body - drain diode reverse drain current	I <sub>DR</sub>	-1.6	А
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu s,$  duty cycle  $\leq$  1%

2. When using the glass epoxy board (FR-4: 40 x 40 x 1 mm)



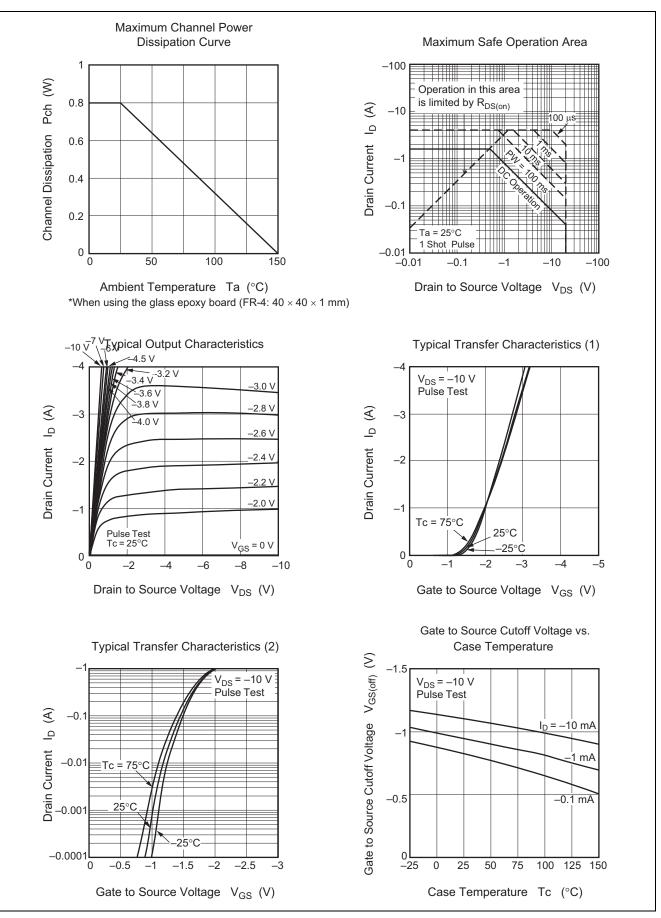
# **Electrical Characteristics**

Item	Symbol	Min	Тур	Max	Unit	Test conditions	
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	-20	_		V	$I_D = -10 \text{ mA}, V_{GS} = 0$	
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	+8	_		V	$I_{G} = +100 \ \mu A, V_{DS} = 0$	
	V <sub>(BR)GSS</sub>	-12	_	_	V	$I_{G} = -100 \ \mu A, V_{DS} = 0$	
Gate to source leak current	I <sub>GSS</sub>	_	_	+10	μA	$V_{GS} = +6 V, V_{DS} = 0$	
	I <sub>GSS</sub>			-10	μA	$V_{GS} = -10 \text{ V}, V_{DS} = 0$	
Drain to source leak current	I <sub>DSS</sub>	_	_	-1	μA	$V_{DS} = -20 V, V_{GS} = 0$	
Gate to source cutoff voltage	V <sub>GS(off)</sub>	-0.4	_	-1.4	V	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	
Drain to source on state resistance	R <sub>DS(on)</sub>		219	280	mΩ	$I_D = -0.8 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$	
	R <sub>DS(on)</sub>	_	363	510	mΩ	$I_D = -0.8 \text{ A}, V_{GS} = -2.5 \text{ V}^{\text{Note3}}$	
Forward transfer admittance	y <sub>fs</sub>	1.3	1.9	_	S	$I_D = -0.8 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$	
Input capacitance	Ciss	_	153	_	pF	$V_{DS} = -10 V$ $V_{GS} = 0$	
Output capacitance	Coss	_	37		pF		
Reverse transfer capacitance	Crss	_	31	_	pF	f = 1 MHz	
Turn - on delay time	t <sub>d(on)</sub>		14		ns	$I_{D} = -0.8 \text{ A} \\ V_{GS} = -4.5 \text{ V} \\ R_{L} = 12.5 \Omega \\ \text{Rg} = 4.7 \Omega$	
Rise time	tr		33		ns		
Turn - off delay time	t <sub>d(off)</sub>		24		ns		
Fall time	t <sub>f</sub>	_	8		ns		
Total gate charge	Qg	_	2.2		nC	$V_{DD} = -10 V$ $V_{GS} = -4.5 V$	
Gate to source charge	Qgs	_	0.5	_	nC		
Gate to drain charge	Qgd	_	0.9		nC	I <sub>D</sub> = -1.6A	
Body - drain diode forward voltage	V <sub>DF</sub>	_	-0.85	-1.1	V	$I_F = -1.6 \text{ A}, V_{GS} = 0^{\text{Note3}}$	

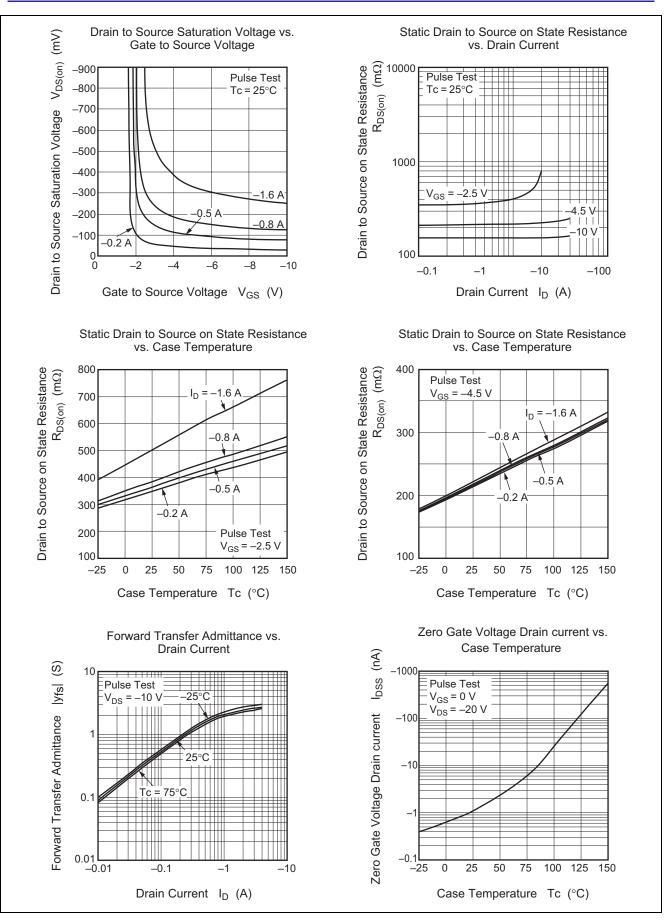
Notes: 3. Pulse test



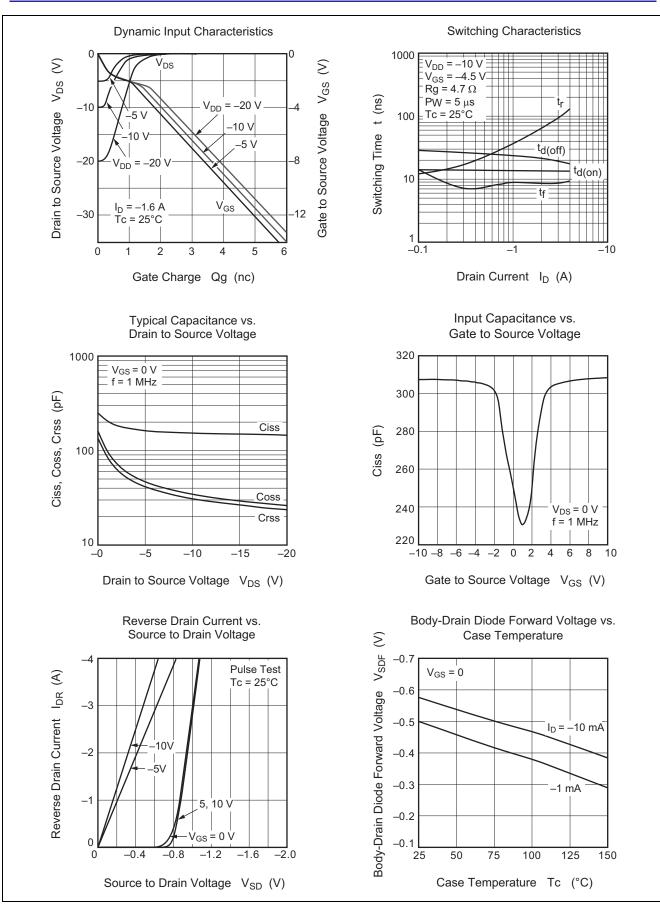
#### **Main Characteristics**



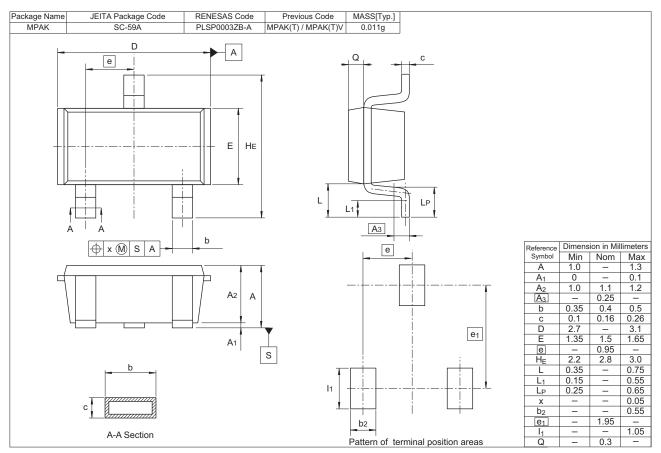








## **Package Dimensions**



# **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
HITJ0204MPTL-HQ	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

Note: This product is designed for consumer use and not for automotive.



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